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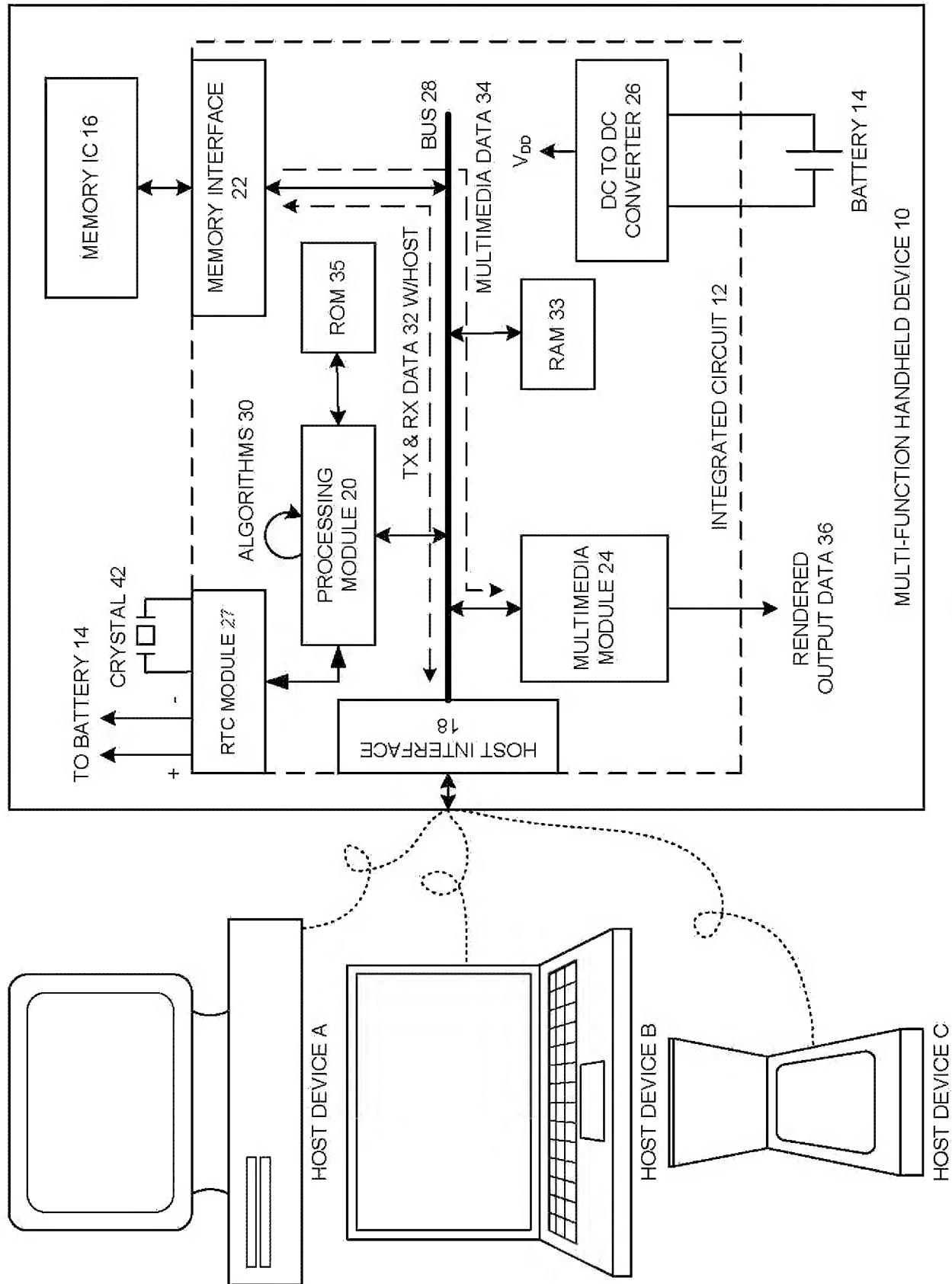


FIG. 1

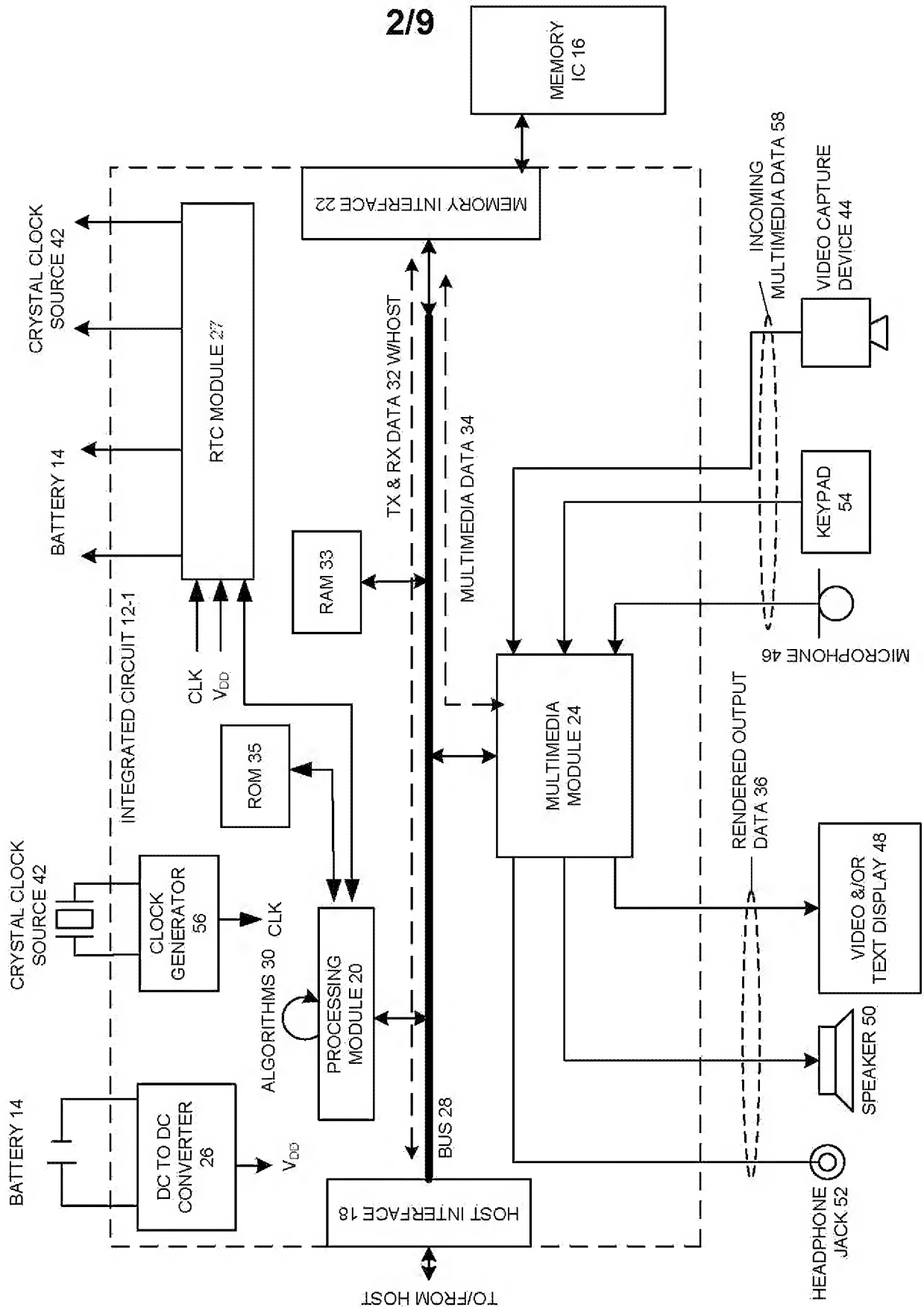
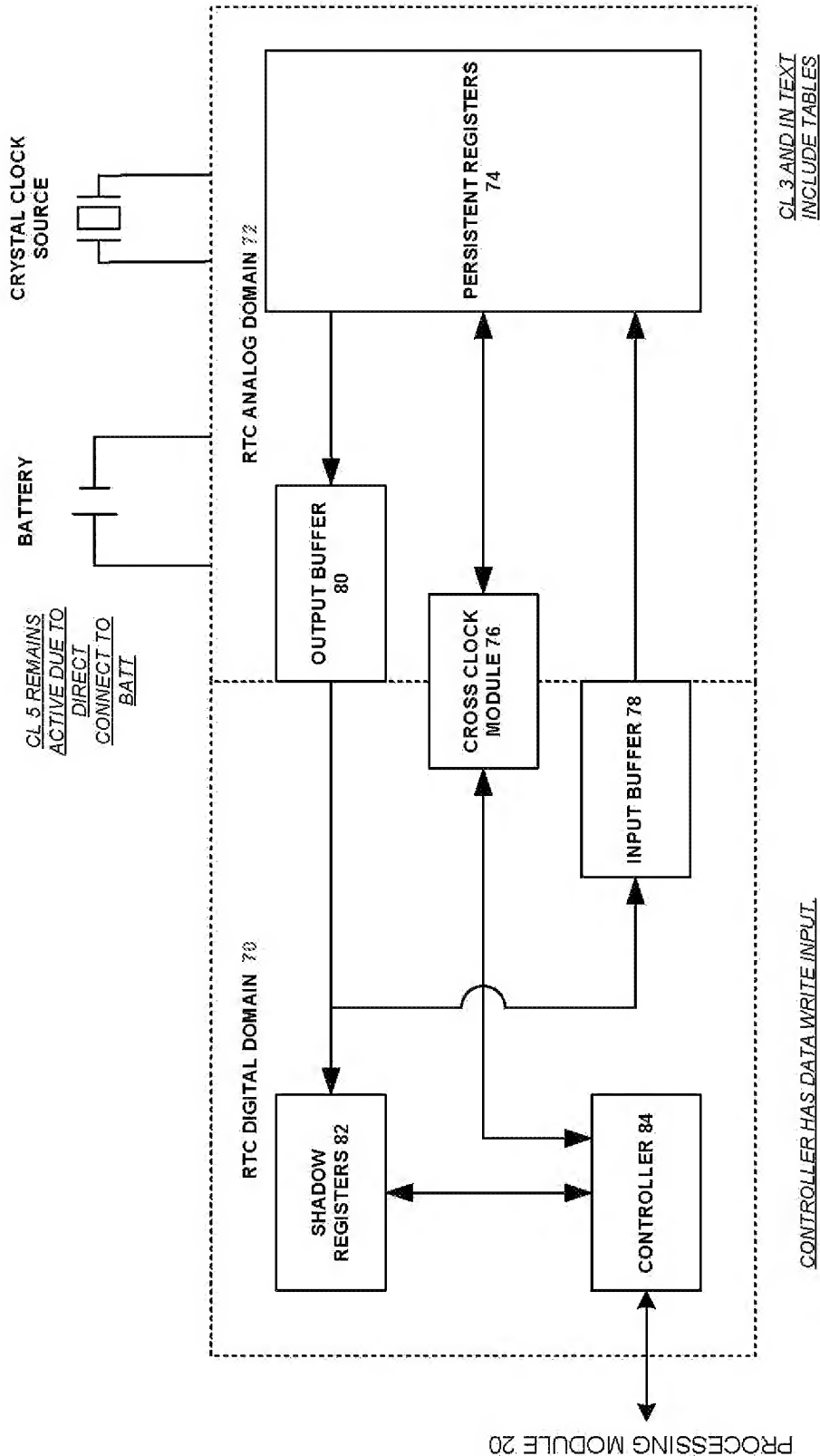


FIG. 2

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CL 3 AND IN TEXT
INCLUDE TABLES

FIG 3
real time clock module 20

CONTROLLER HAS DATA WRITE INPUT,
DATA READ OUTPUT, READ REQUEST,
WRITE REQUEST, AND RW ACK

DISCUSS W/ REFERENCE TO FIGURE,
CLAIMS 8 - 13, AS SEPARATE
EMBODIMENTS OF DATA EXCHANGE

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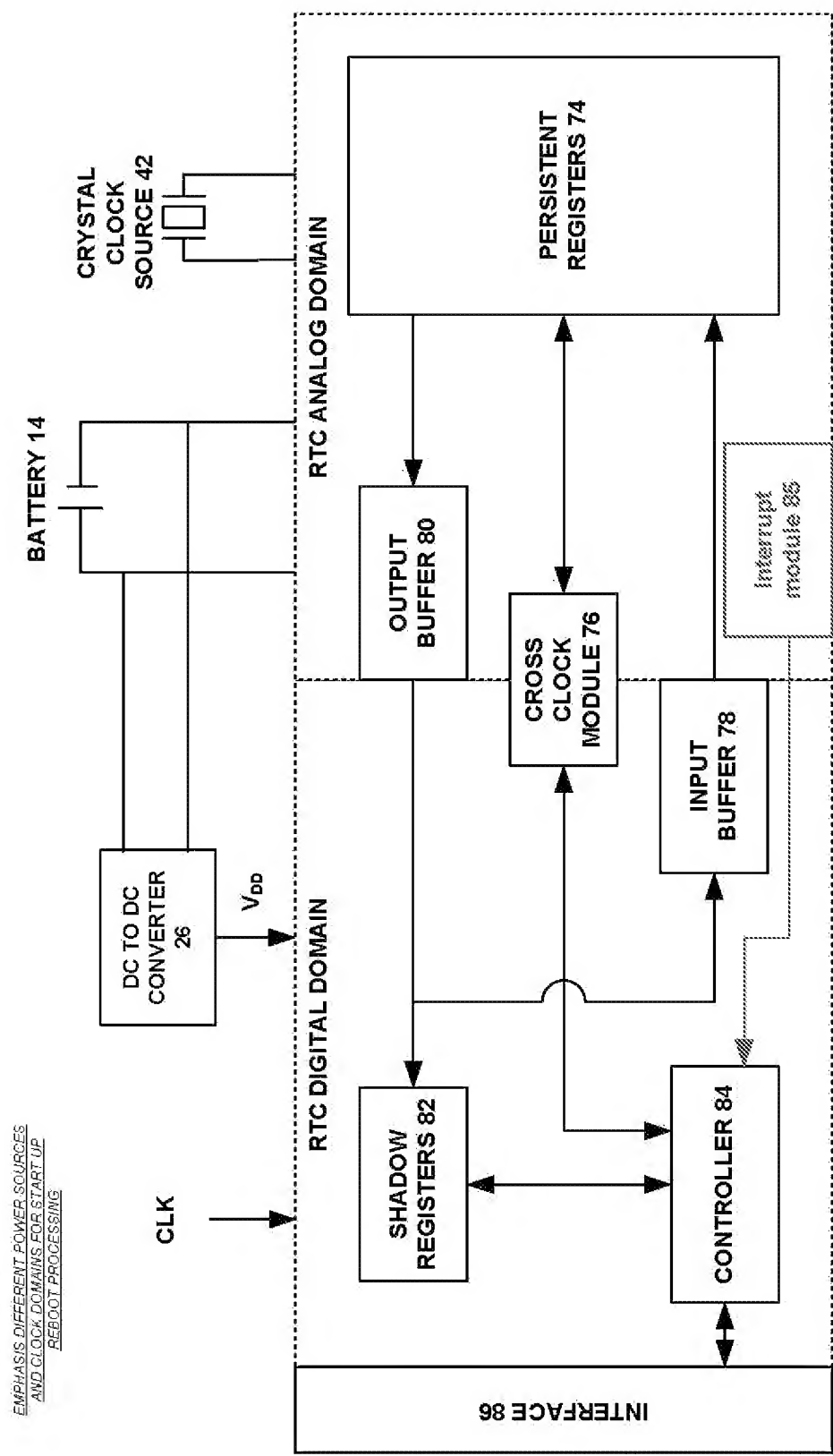


FIG 4
real time clock module 27

EMPHASIS DIFFERENT POWER SOURCES
AND CLOCK DOMAINS FOR START UP
REBOOT PROCESSING

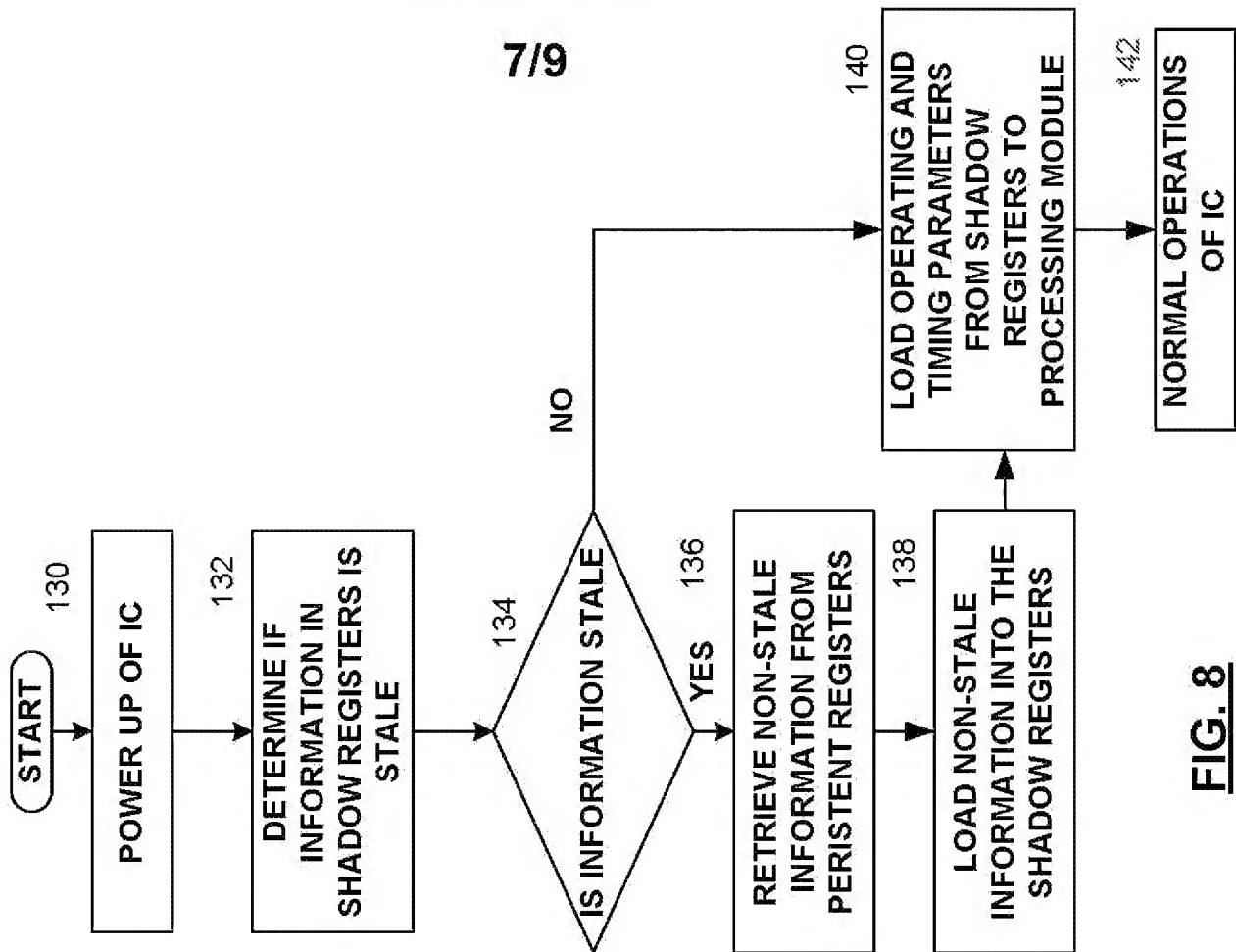


FIG. 8

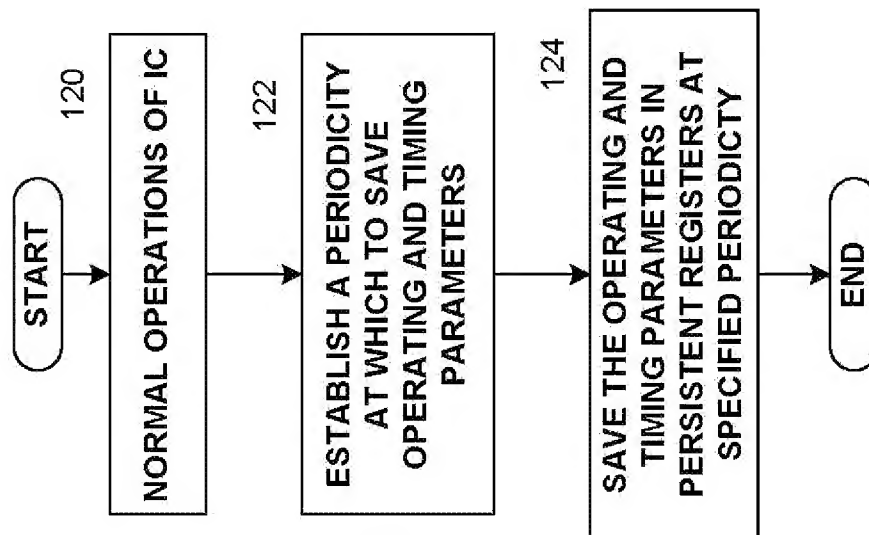


FIG. 7

REFERENCE THE
 TABLE THAT LISTS
 THESE
 PARAMETERS

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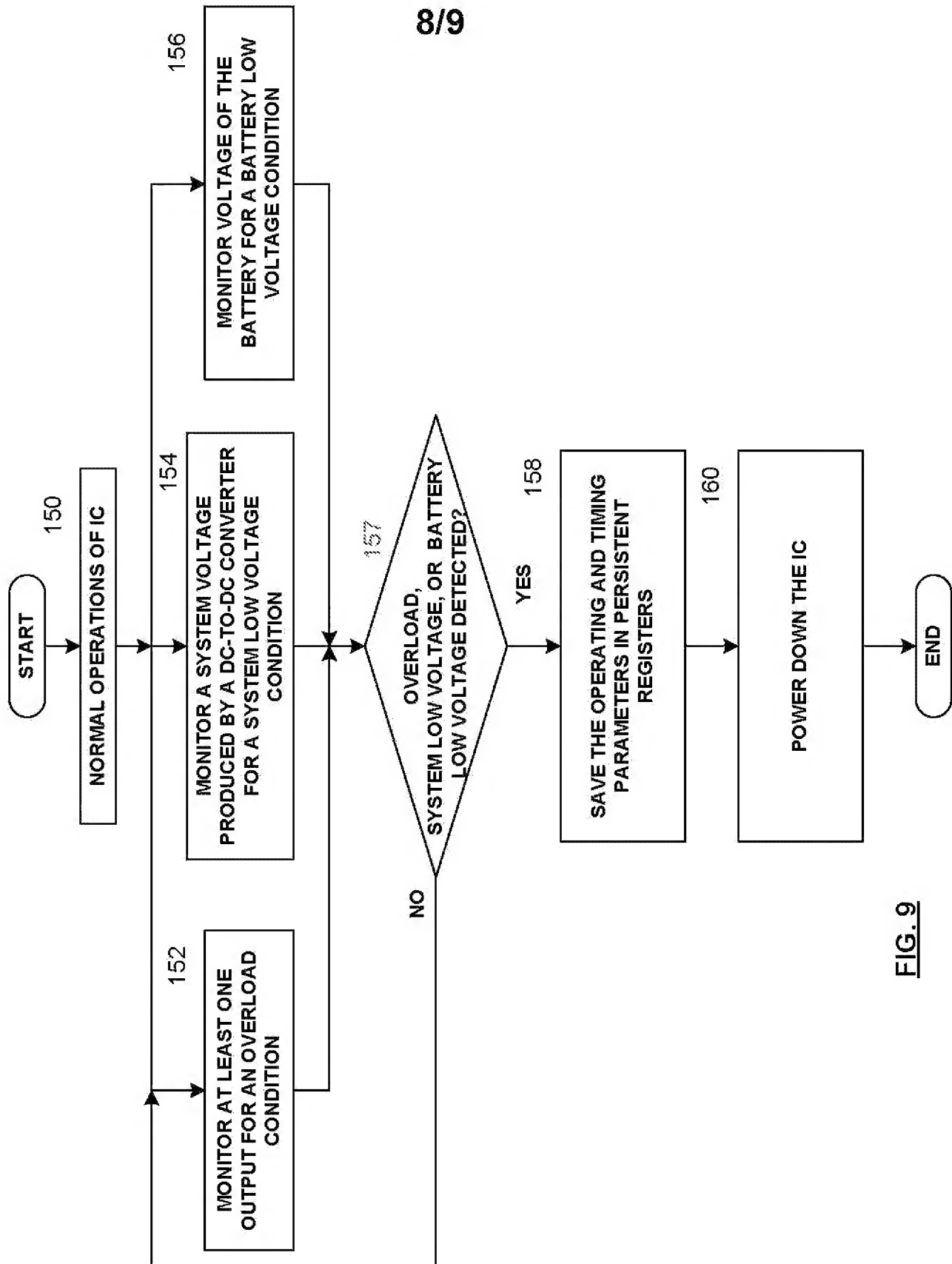


FIG. 9